# All India Council for Technical Education





Nelson Mandela Marg, Vasant Kunj, New Delhi-110070 Website: www.aicte-india.org

#### **APPROVAL PROCESS 2024-25**

# **Extension of Approval (EoA)**

F.No. Western/1-43669205005/2024/EOA

Date of Approval: 08-May-2024

To,

The Secretary, Tech. & Higher Education Deptt. Govt. of Maharashta, Mantralaya, Annexe Building, Mumbai-400032

### Sub: Extension of Approval for the Academic Year 2024-25

Ref: Online application of the Institution submitted for Extension of Approval for the Academic Year 2024-25

Sir/Madam,

In terms of the provisions under the All India Council for Technical Education (Grant of Approvals for Technical Education), Powers delegated in AICTE ACT 1987, (No 52 of 1987) chapter II - u/s 2(g) to regulate Technical and subsequent Regulations of AICTE, I am directed to convey the approval to:

Permanent Id	1-7118101	Application Id	1-43669205005	
Name of the Institute	VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY	Name of the Society/Trust	VIVEKANAND EDUCATION SOCIETY	
Institute Address	HASHU ADVANI MEMORIAL COMPLEX, COLLECTOR'S COLONY, R.C. MARG, CHEMBUR, MUMBAI - 400074, MUMBAI, MUMBAI SUBURBAN, Maharashtra, 400074	Society/Trust Address	1ST FLOOR , ANNEX BLDG., VES COLLEGE CAMPUS, SINDHI SOCIETY, CHEMBUR, MUMBAI - 400071,CHEMBUR,MUMBAI SUBURBAN,Maharashtra,400071	
Institute Type	Private-Self Financing	Region	Western	
Year of Establishment	1992			

#### To conduct following Programs/Courses with the Intake indicated below for the Academic Year 2024-25

Level	Program	Course	Affiliating Body (University/Body)	Intake Approved for 2023-24	Intake Approved for 2024-25	NRI Approval Status	FN / Gulf quota/ OCI/ Approval Status
UNDER GRADUATE	ENGINEERING AND TECHNOLOGY	ARTIFICIAL INTELLIGENCE AND DATA SCIENCE	University Of Mumbai, Mumbai	120	120	No	No
UNDER GRADUATE	ENGINEERING AND TECHNOLOGY	AUTOMATION AND ROBOTICS	Mumbai University, Mumbai	60	60	No	No
UNDER GRADUATE	ENGINEERING AND TECHNOLOGY	COMPUTER ENGINEERING	Mumbai University, Mumbai	180	180	No	No
UNDER GRADUATE	ENGINEERING AND TECHNOLOGY	ELECTRONICS AND COMPUTER SCIENCE	Mumbai University, Mumbai	60	60	No	No

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UNDER GRADUATE	ENGINEERING AND TECHNOLOGY	ELECTRONICS AND TELECOMMUNICA TIONS ENGINEERING	Mumbai University, Mumbai	120	120	No	No
UNDER GRADUATE	ENGINEERING AND TECHNOLOGY	INFORMATION TECHNOLOGY	Mumbai University, Mumbai	180	180	No	No
POST GRADUATE	COMPUTER APPLICATION S	MASTER OF COMPUTER APPLICATIONS	Mumbai University, Mumbai	60	60	No	No
POST GRADUATE	COMPUTER APPLICATION S	MASTER OF COMPUTER APPLICATIONS	Mumbai University, Mumbai	60	60	No	No
POST GRADUATE	ENGINEERING AND TECHNOLOGY	INFORMATION TECHNOLOGY	Mumbai University, Mumbai	6	6	No	No
POST GRADUATE	ENGINEERING AND TECHNOLOGY	VLSI AND EMBEDDED SYSTEMS	Mumbai University, Mumbai	6	6	No	No

#### Course(s) Applied for Closure by the Institution for the Academic Year 2024-25

Level	Program	Course	Affiliating Body (Univ/Body)	Course Closure Status	Intake Approved for 2024-25
POST GRADUATE	ENGINEERING AND TECHNOLOGY	INSTRUMENTATION & CONTROL	Mumbai University, Mumbai	Approved	0

All AICTE approved Institutions are empowered to nurture ecosystems for Skilling (through Vocational courses) via making effective use of existing infrastructure facilities and human resources.

It is mandatory to comply all the essential requirements as given in APH 2024-25 to 2027 (Chapter- VI)

The Institution/ University is having the following deficiencies as per the online application submitted to AICTE and the same shall be complied within One year from the date of issue of this EoA

### **Deficiencies Noted based on Self Disclosure**

**Faculty Deficiency** 

\*Please refer Deficiency Report for details

## **Important Instructions**

- 1. As per mandatory Disclosure of APH 2024-27(Annexure-18, page180) Institutions must disclose the following information submitted to Council at the Prominent location on its website.
  - j. Department wise availability of Infrastructure along with approved courses and intake approved by the Council.
  - ii. Faculty details: Department wise: Name& Designation of the faculty members/teaching staff along with their qualification, tenure of service in your organization, total experience, Institution should also disclose Student Faculty Ratio, Cadre Ratio.
  - iii. Additionally Audited Financial Statements for last 3 Financial years.
- 2. Reservation Policy of the Central Government (Including EWS) / Respective State Government/ UT as the case shall be applicable to all the Programmes. The concerned State Government/ UT Admission authority shall decide Modalities of Admission.
- 3. The Institution offering courses earlier in the Regular Shift, First Shift, Second Shift/Part Time are now amalgamated as total intake and shall have to fulfil all facilities such as Infrastructure, Faculty and other requirements as per the norms specified in the Approval Process Handbook 2024-25 to 2027 for the Total Approved Intake.
- 4. In case of any differences in content in this Computer generated Extension of Approval Letter, the content/information as approved by the Executive Council / General Council as available on the record of AICTE shall be final and binding.
- 5. All AICTE institutions are highly encouraged to get NBA/NAAC accreditation. All eligible AICTE institutions are thoroughly encouraged to participate in NIRF ranking process.
- 6. Deemed to be University: Institutions Deemed to be Universities (Running Technical Education Programmes), it is mandatory to have AICTE approval from the Academic Year 2018-19 in compliance of the Hon'ble Supreme Court Order dated 03-11-2017 passed in CA No.17869- 17870 /2017.
- 7. AICTE Approved Institutes are encouraged to utilize SWAYAM PLUS Courses up-to 40%
- 8. Internship is mandatory for all admitted students.
- 9. AICTE Approved Institutes are encouraged to make efficient use of the flagship schemes like:
  - a. Parakh: Student Gap analysis portal bases services.
  - b. Students Scholarship schemes like Pragati, Saksham, Swanath, ADF, etc.
  - c. Course in Indian Languages.
  - d. ATAL FDPs: Faculty training for Emerging areas and cutting edge Technologies.
  - e. Augmenting Utilization of Research Assets (AURA).
  - f. Smart India Hackathon: World's largest Open Innovation Platform.

Prof.Rajive Kumar Member Secretary, AICTE

# Copy to:

#### 1. The Director of Technical Education\*\*, Maharashtra

#### 2. The Registrar\*\*,

Mumbai University, Mumbai

#### 3. The Principal / Director,

VIVEKANAND EDUCATION SOCIETY'S INSTITUTE OF TECHNOLOGY
Hashu Advani Memorial Complex, Collector'S Colony, R.C. Marg, Chembur, Mumbai - 400074,
Mumbai, Mumbai Suburban,
Maharashtra, 400074

#### 4. The Secretary / Chairman,

VIVEKANAND EDUCATION SOCIETY

1St Floor , Annex Bldg., Ves College Campus, Sindhi Society, Chembur, Mumbai - 400071, Chembur, Mumbai Suburban ,
Maharashtra, 400071

#### 5. Guard File(AICTE)

Note: Validity of the Course details may be verified at <a href="http://www.aicte-india.org/">http://www.aicte-india.org/</a>

This is a computer generated Statement. No signature Required

<sup>\*\*</sup> Individual Approval letter copy will not be communicated through Post/Email. However, a consolidated list of Approved Institutions(bulk) may be downloaded from the respective login id's.